

Product data sheet

1. General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. This "series B" triac will commutate the full rated RMS current at the maximum rated junction temperature without the aid of a snubber.

2. Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High immunity to false turn-on by dV/dt
- High voltage capability
- Less sensitive gate for very high noise immunity
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

3. Applications

- Electronic thermostats
- General purpose motor controls
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DRM}	repetitive peak off- state voltage		-	-	600	V
I _{TSM}	non-repetitive peak on- state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; \text{ Fig. 4}; \text{ Fig. 5}$	-	-	65	A
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 102 \text{ °C}$; Fig. 1; Fig. 2; Fig. 3	-	-	8	A
Static char	acteristics	· · · · · ·	'			
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T}2+ \text{ G}+;$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	2	18	50	mA
		V_D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 7</u>	2	21	50	mA





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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V _D = 12 V; I _T = 0.1 A; T2- G-;	2	34	50	mA
		T _j = 25 °C; <u>Fig. 7</u>				

5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2
2	T2	main terminal 2		sym051
3	G	gate		
mb	T2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

6. Ordering information

Table 3.Ordering information

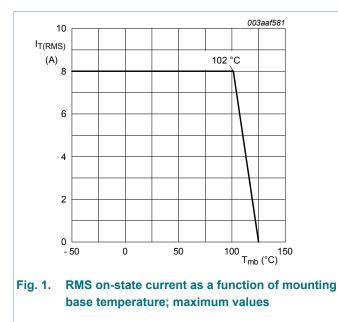
Type number	Package				
	Name	Description	Version		
BTA208-600B	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78		

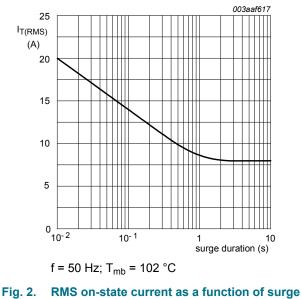
7. Limiting values

Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

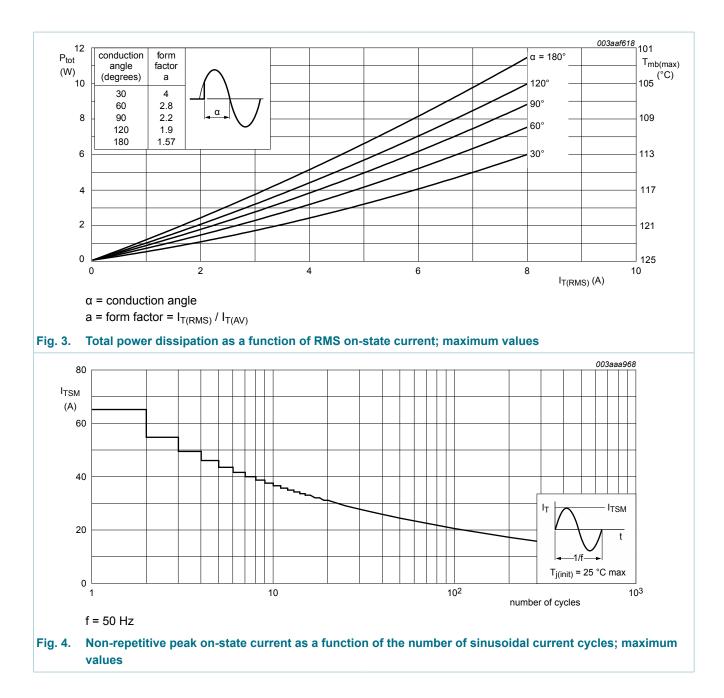
Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DRM}	repetitive peak off-state voltage		-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{mb} ≤ 102 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	8	A
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ ##C};$ $t_p = 20 \text{ ms}; \text{Fig. 4}; \text{Fig. 5}$	-	65	A
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 16.7 \text{ ms}$	-	71	A
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	21	A ² s
dl _T /dt	rate of rise of on-state current	I_T = 12 A; I_G = 0.2 A; dI_G/dt = 0.2 A/µs	-	100	A/µs
I _{GM}	peak gate current		-	2	А
P _{GM}	peak gate power		-	5	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C





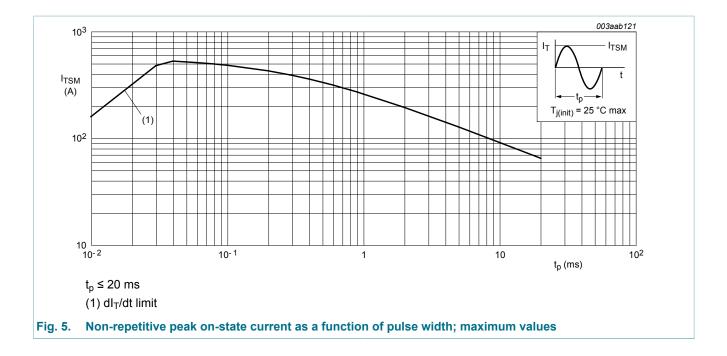
duration; maximum values

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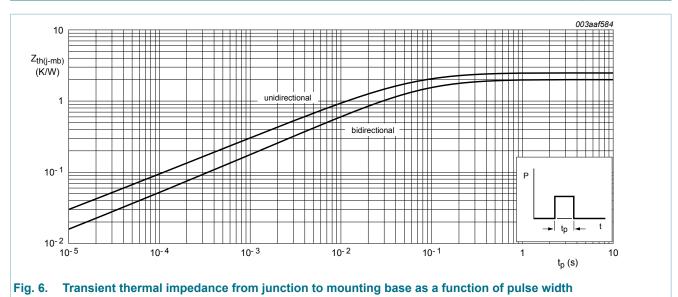
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8. Thermal characteristics

Table 5. T	hermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance	full cycle; Fig. 6	-	-	2	K/W
	from junction to mounting base	half cycle; <u>Fig. 6</u>	-	-	2.4	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W



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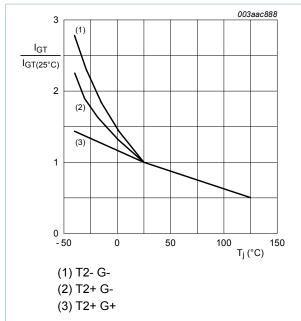
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9. Characteristics

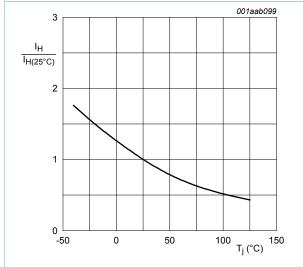
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	cteristics						
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 7</u>		2	18	50	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 7</u>		2	21	50	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 7</u>		2	34	50	mA
ΙL	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 8</u>		-	31	60	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 8</u>		-	34	90	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 8</u>		-	30	60	mA
н	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>		-	31	60	mA
VT	on-state voltage	I _T = 10 A; T _j = 25 °C; <u>Fig. 10</u>		-	1.3	1.65	V
/ _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; Fig. 11		-	0.7	1	V
		V _D = 400 V; I _T = 0.1 A; T _j = 125 °C; Fig. 11		0.25	0.4	-	V
D	off-state current	V _D = 600 V; T _j = 125 °C		-	0.1	0.5	mA
Dynamic ch	aracteristics	· · · · · · · · · · · · · · · · · · ·	II				
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 402 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit		1000	4000	-	V/µs
dl _{com} /dt	rate of change of commutating current	V_D = 400 V; T_j = 125 °C; $I_{T(RMS)}$ = 8 A; dV _{com} /dt = 20 V/µs; gate open circuit; snubberless condition; <u>Fig. 12</u>		-	14	-	A/m
lgt	gate-controlled turn-on time	I_{TM} = 12 A; V_D = 600 V; I_G = 0.1 A; dI_G/dt = 5 A/µs		-	2	-	μs

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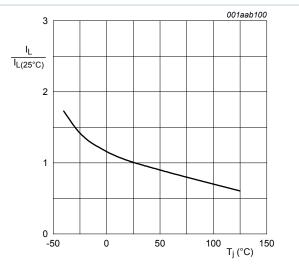
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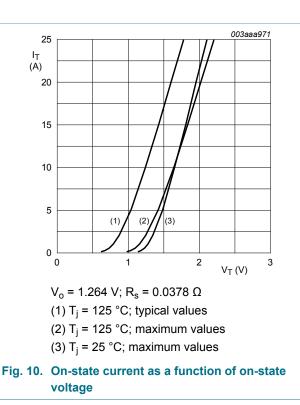








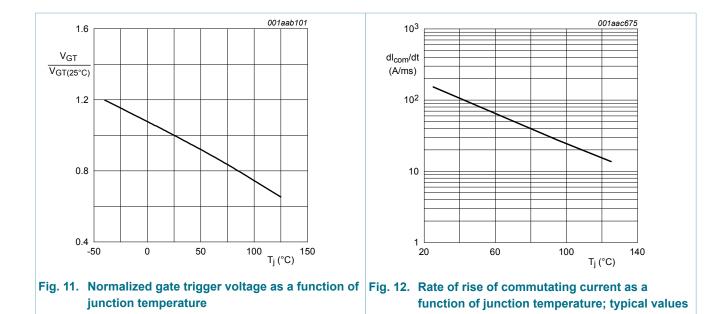




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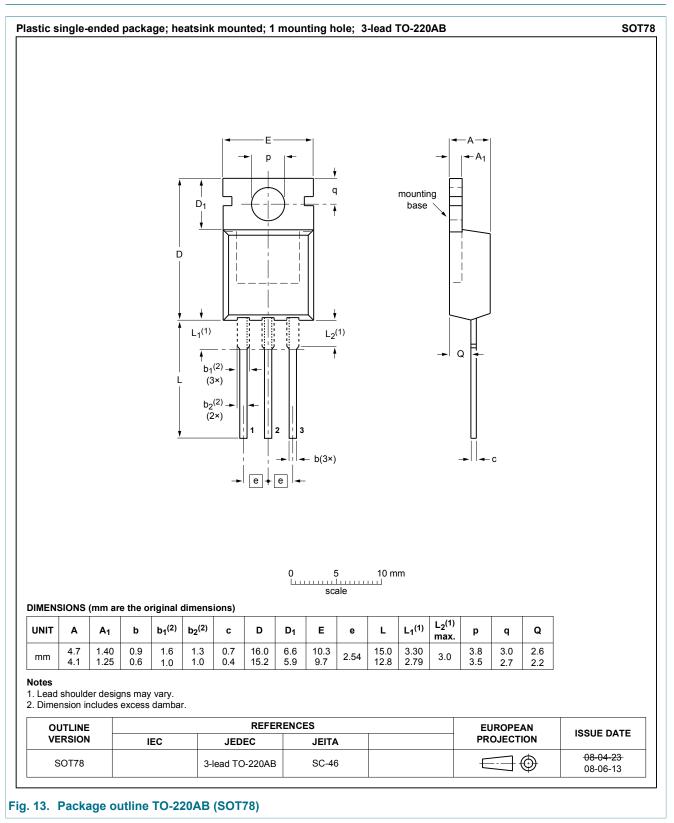
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10. Package outline



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Document status [1][2]	Product status [<u>3]</u>	Definition
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